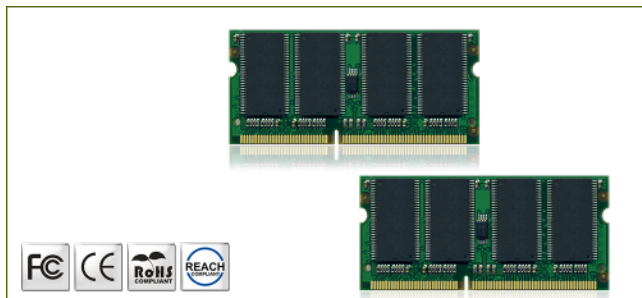


### Features



- Single Pulsed RAS-interface
- Fully Synchronous to positive CLK edge
- 4 banks controlled by BA0 & BA1
- Multiple Burst Read with single write option
- Automatic and controlled pre-charge command
- Intend for 133 MHz applications
- Auto refresh (CBR) and Self-Refresh
- Inputs and Outputs are LVTTTL compatible
- VDD=VDDQ= 3.3 Volt ± 0.3
- 30u" gold fingers

### Specification

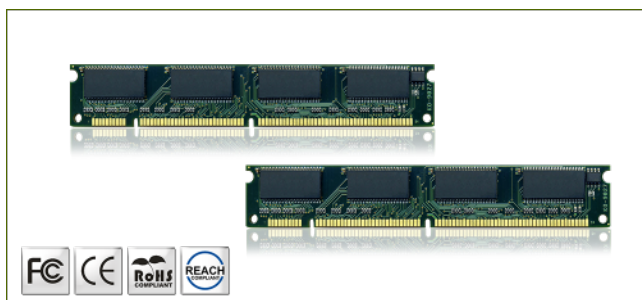
#### SDRAM SO-DIMM Standard grade

■ <b>Interface</b>	JEDEC Standard 144-pin Dual In-Line Memory Module
■ <b>IC Data Rate</b>	SDR-133
■ <b>Bandwidth</b>	64 bits
■ <b>DIMM Data Rate</b>	PC133 / PC100
■ <b>Capacity</b>	128MB / 256MB
■ <b>Pin Number</b>	144 Pins
■ <b>Height</b>	30 mm + 0.15 mm
■ <b>Voltage</b>	3.3V ± 0.3V
■ <b>Warranty</b>	5 years

#### SDRAM SO-DIMM Industrial grade

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■ <b>IC Data Rate</b>	SDR-133
■ <b>Bandwidth</b>	64 bits
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### Specification

#### SDRAM U-DIMM Standard grade

■ <b>Interface</b>	JEDEC Standard 168-pin Dual In-Line Memory Module
■ <b>IC Data Rate</b>	SDR-133
■ <b>Bandwidth</b>	64 bits
■ <b>DIMM Data Rate</b>	PC133 / PC100
■ <b>Capacity</b>	128MB / 256MB
■ <b>Pin Number</b>	168 Pins
■ <b>Height</b>	21.90 mm + 0.15 mm
■ <b>Voltage</b>	3.3V ± 0.3V
■ <b>Warranty</b>	5 years

#### SDRAM U-DIMM Industrial grade

■ <b>Interface</b>	JEDEC Standard 168-pin Dual In-Line Memory Module
■ <b>IC Data Rate</b>	SDR-133
■ <b>Bandwidth</b>	64 bits
■ <b>DIMM Data Rate</b>	PC133 / PC100
■ <b>Capacity</b>	128MB / 256MB
■ <b>Pin Number</b>	168 Pins
■ <b>Height</b>	21.90 mm + 0.15 mm
■ <b>Voltage</b>	3.3V ± 0.3V
■ <b>Warranty</b>	5 years